

FIG.1

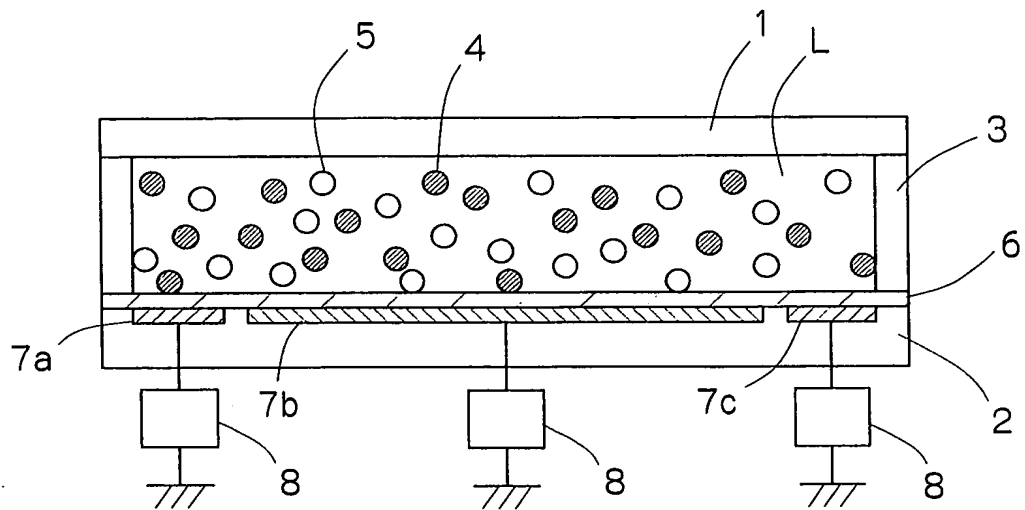


FIG.2

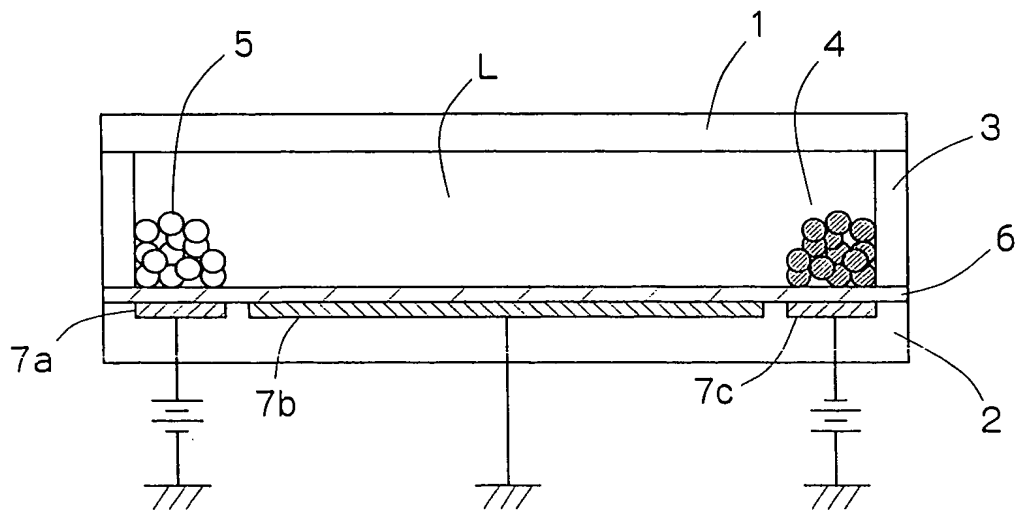


FIG.3

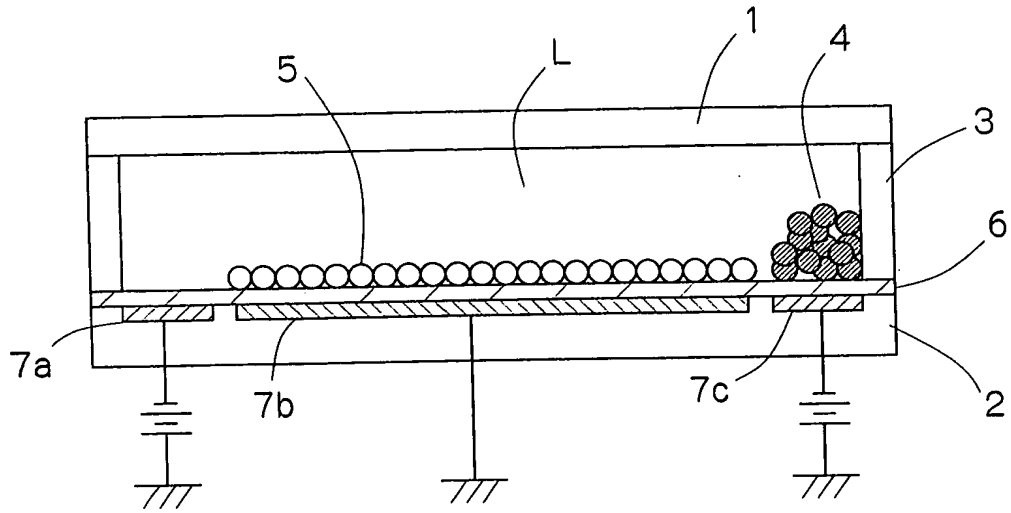


FIG.4

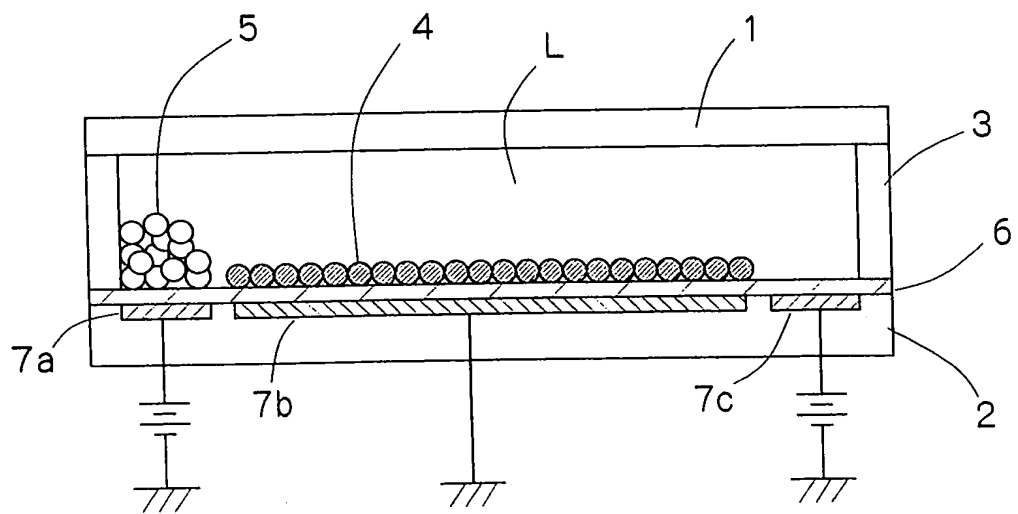


FIG.5

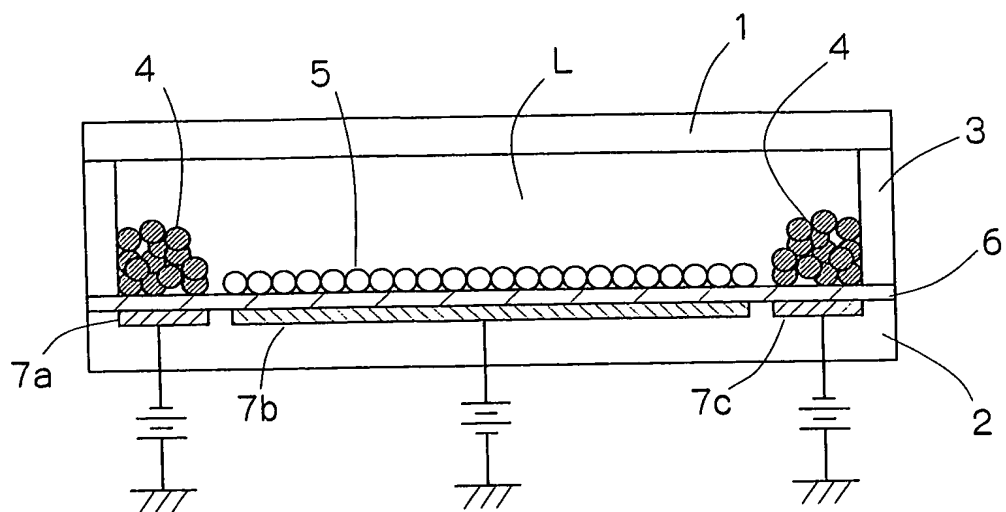


FIG.6

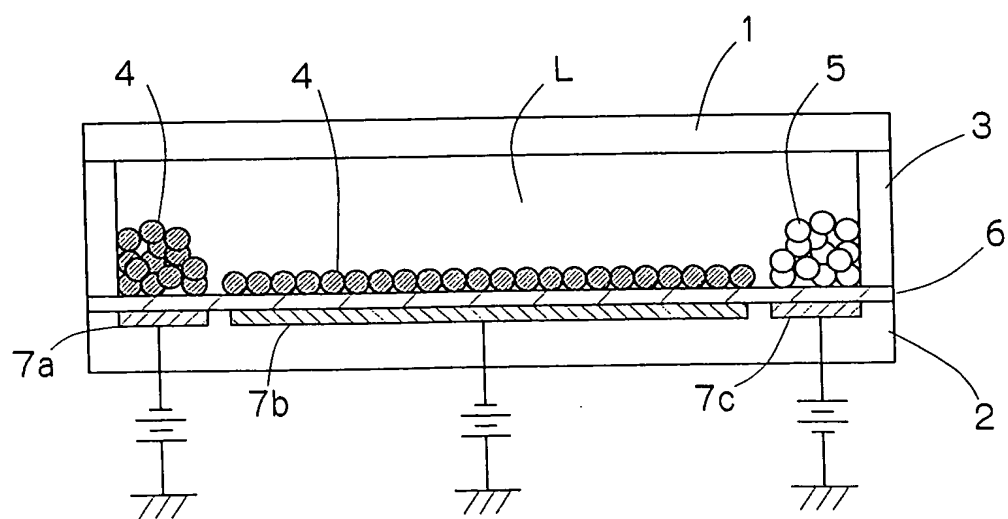


FIG.7

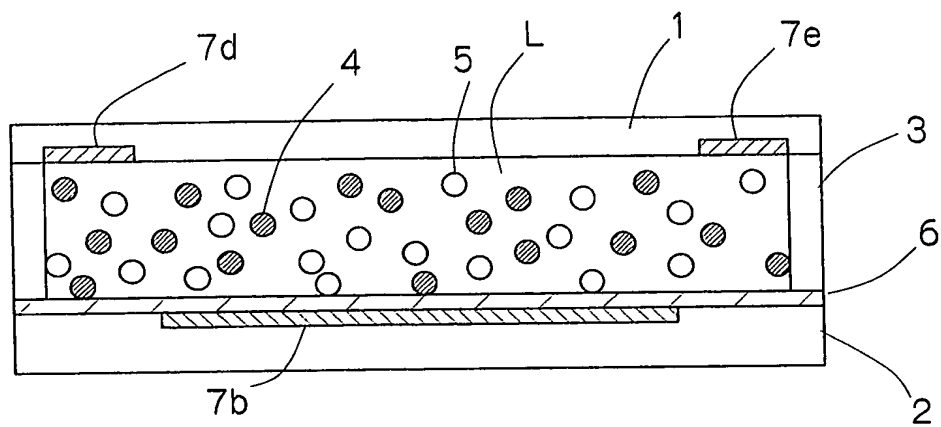


FIG.8

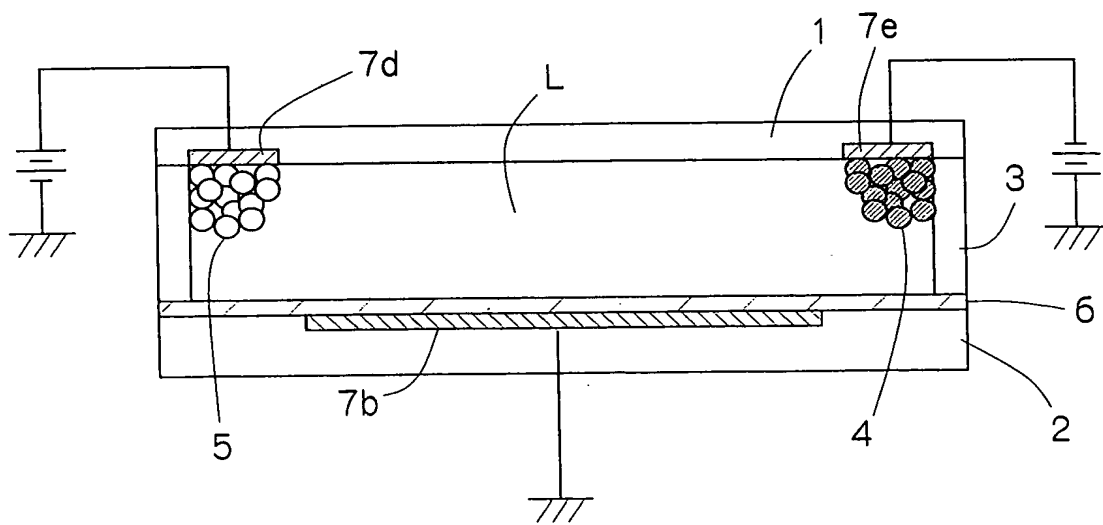


FIG.9

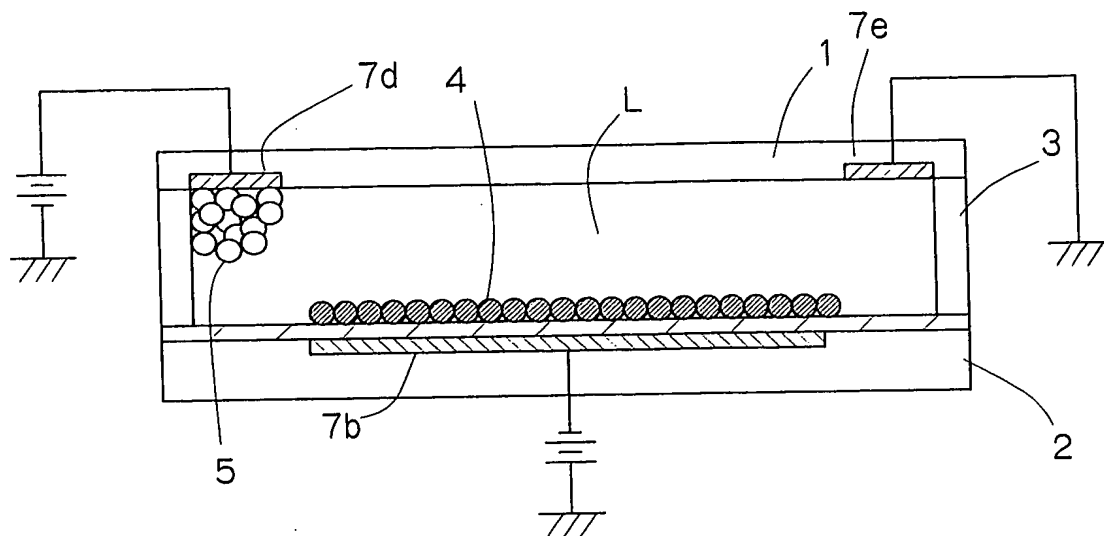


FIG.10

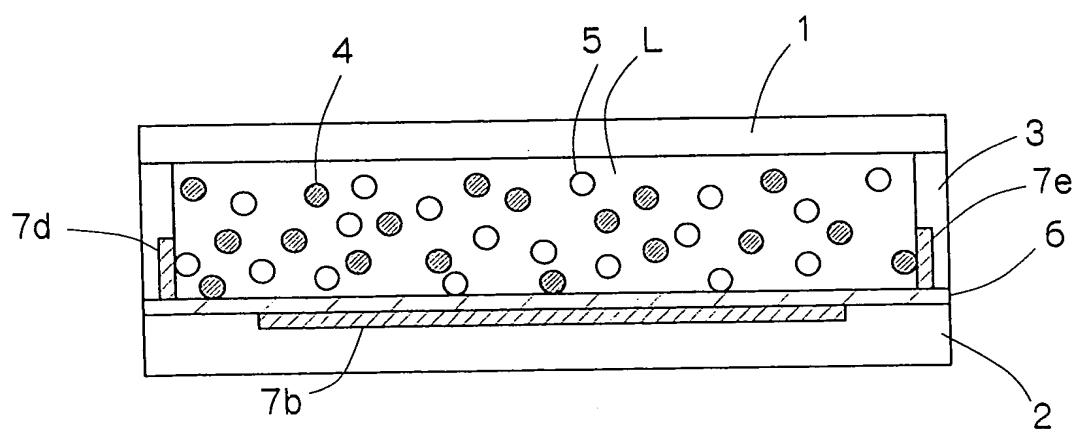


FIG.11

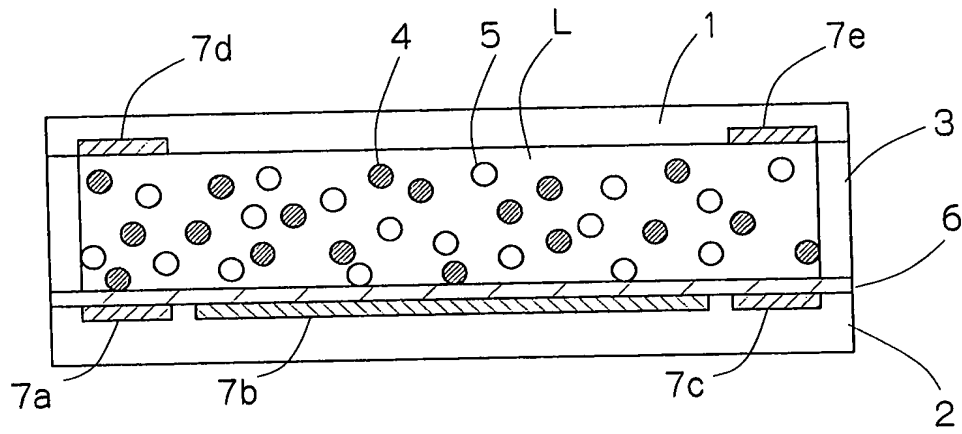


FIG.12

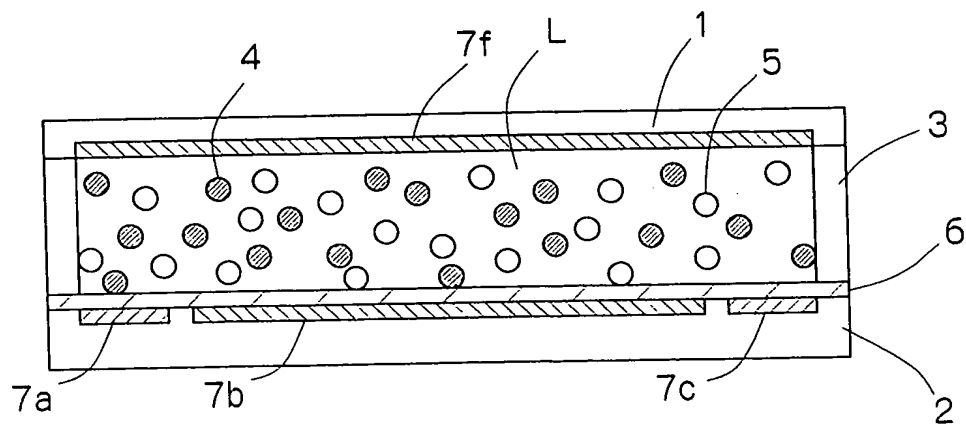


FIG.13

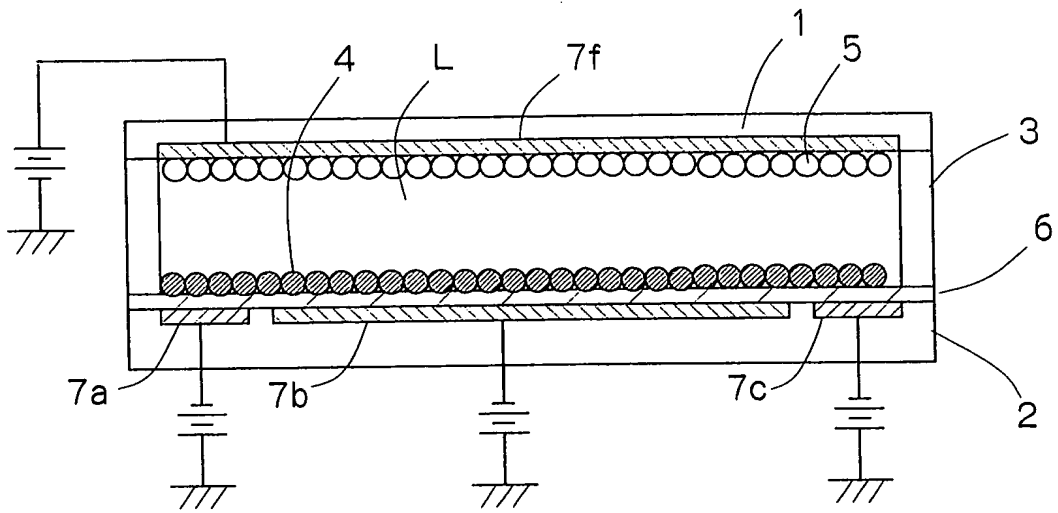


FIG.14

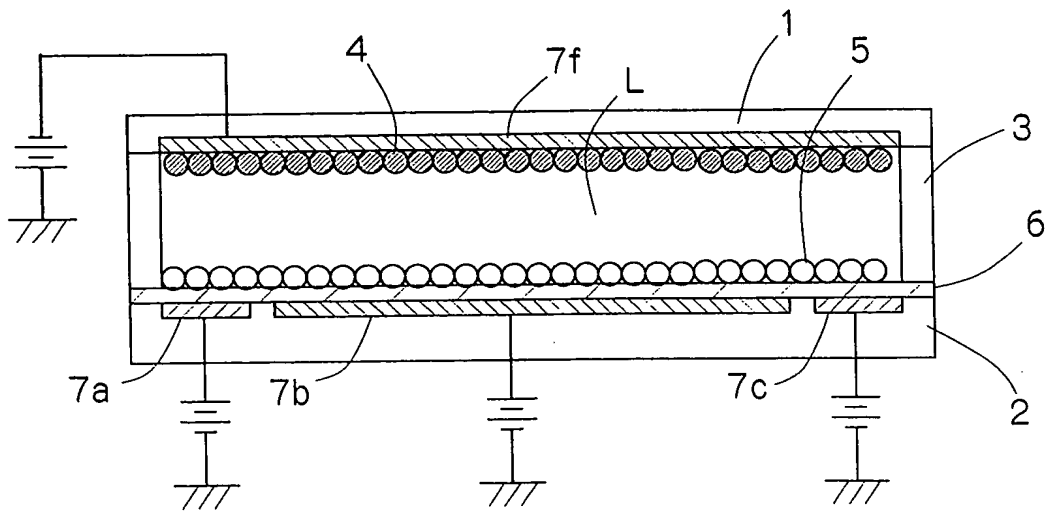


FIG.15

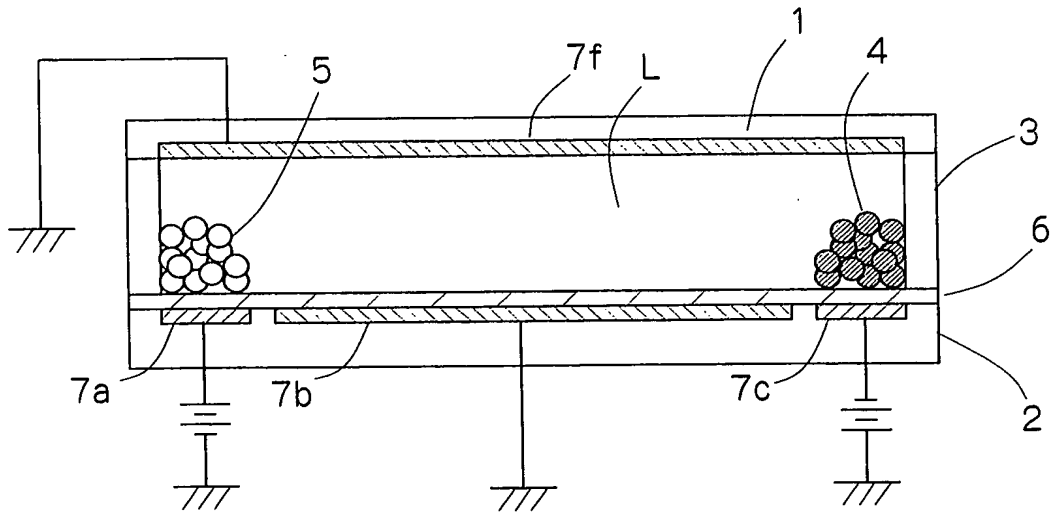
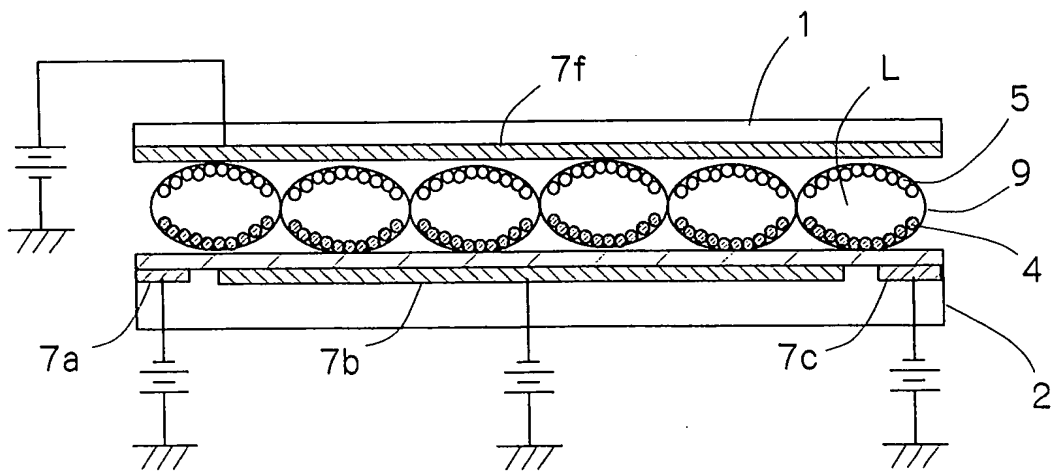


FIG.16





This cross-sectional view shows a device with a central layer 1. Above and below this layer are electrodes 4 and 5, respectively. A series of oval structures 9 are positioned between the electrodes. Labels 7a, 7b, 7c, 7f, and L indicate specific regions and components. The device is connected to a power source and ground.

A cross-sectional view of a device. A central layer 1 contains a series of oval regions 4. Layer 1 is sandwiched between layers 5 and 9. Layer 5 has a pattern of small circles 7f. Layer 9 has a pattern of small circles 7a, 7b, and 7c. The device is connected to ground via a wire and three battery symbols.

FIG.19

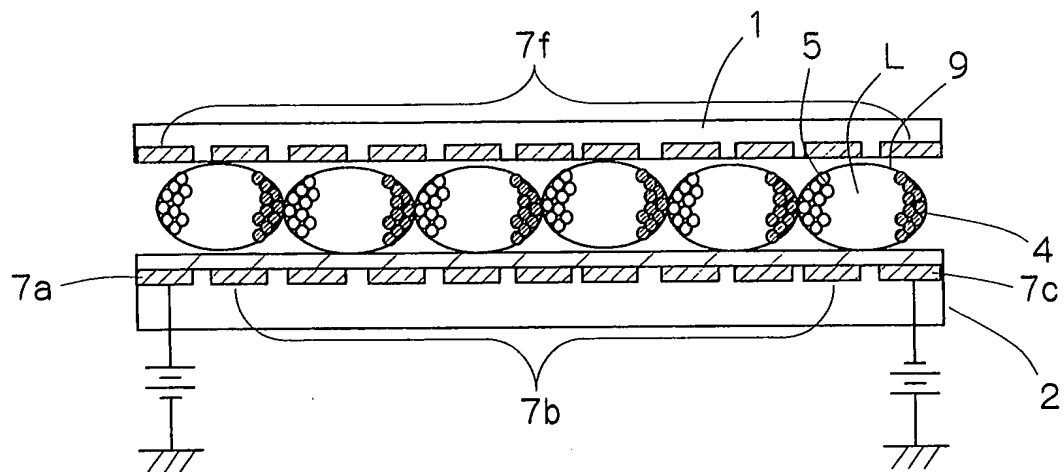


FIG.20

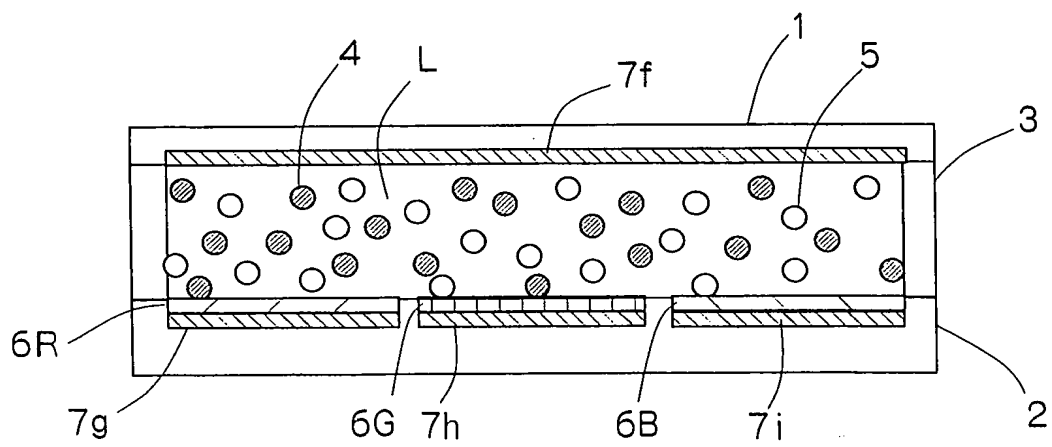


FIG.21

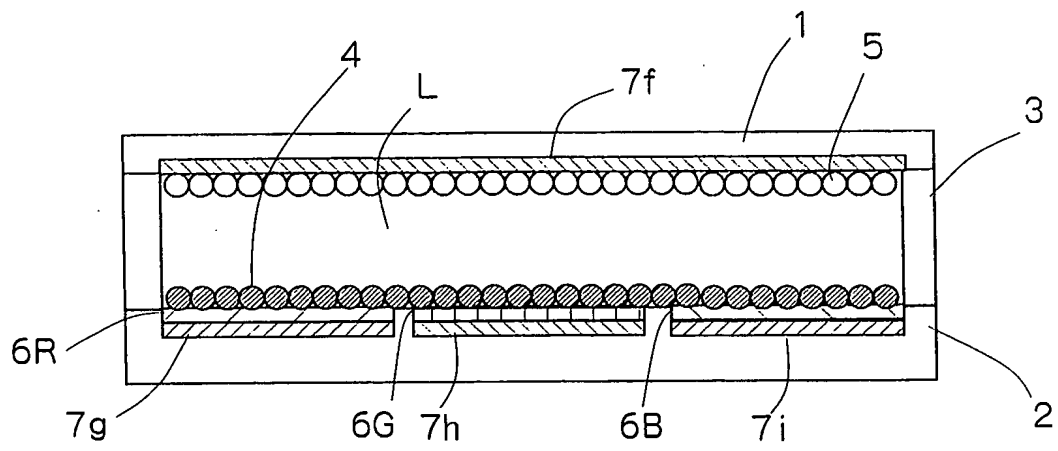
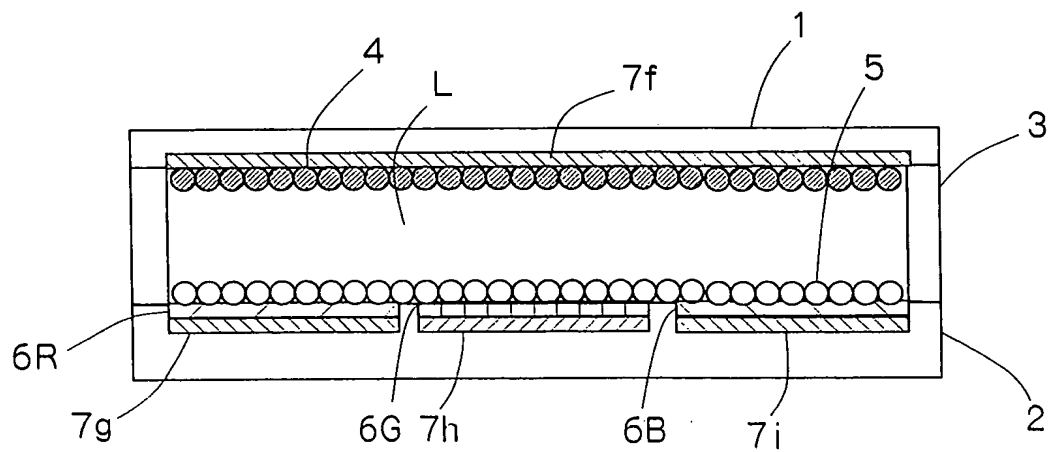


FIG.22



A cross-sectional view of a semiconductor device 100. The device is contained within a rectangular frame 2. It features a central channel 1. On the left side of the channel, there is a stack of layers: a bottom layer 6R, a middle layer 6G, and a top layer 5. A label 'L' points to the channel region. On the right side, there is another stack of layers: a bottom layer 6B, a middle layer 6i, and a top layer 4. A label '7f' points to the channel region. A label '3' points to the right side of the frame. A label '7g' points to the bottom layer 6R. A label '7h' points to the middle layer 6G. A label '7i' points to the middle layer 6i.

FIG.25

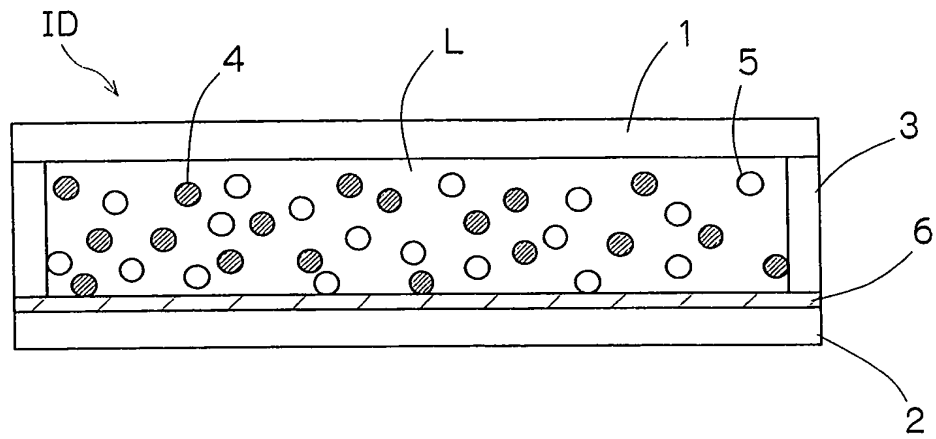


FIG.26

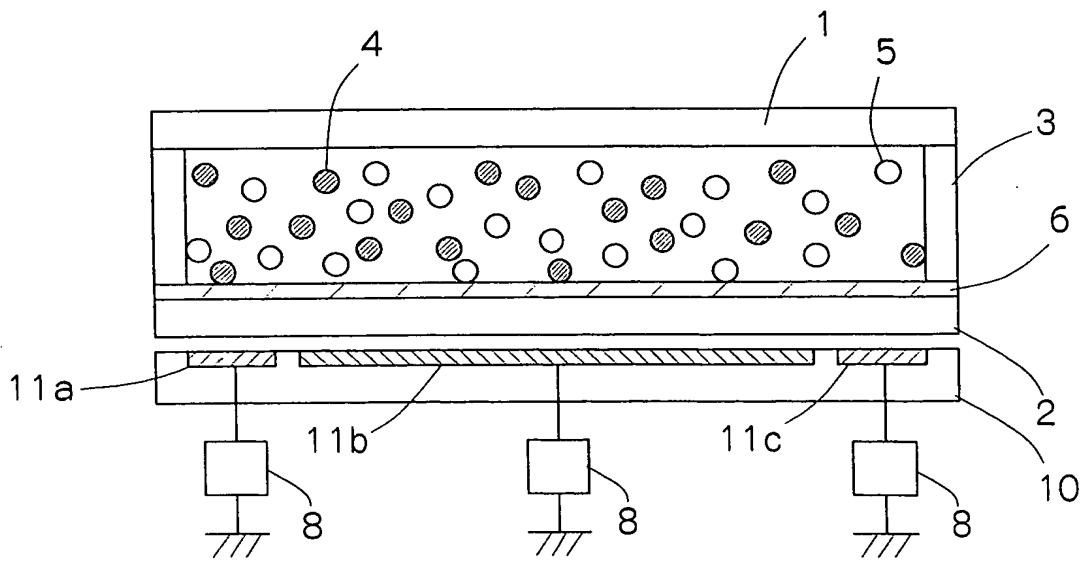


FIG.27A

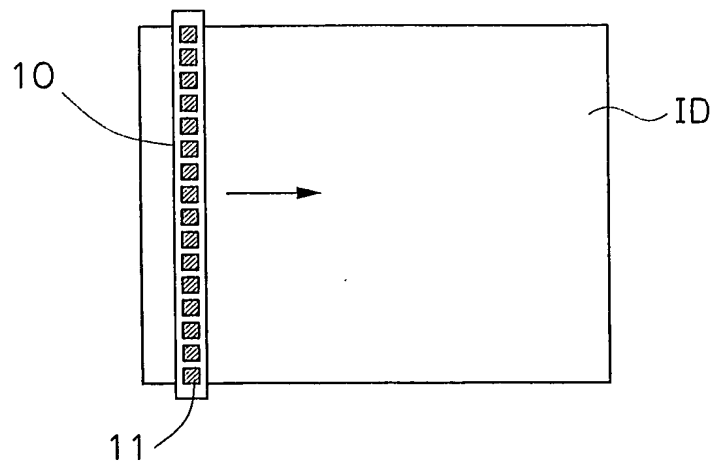


FIG.27B

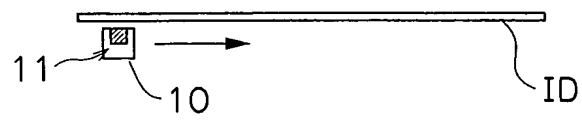


FIG.28

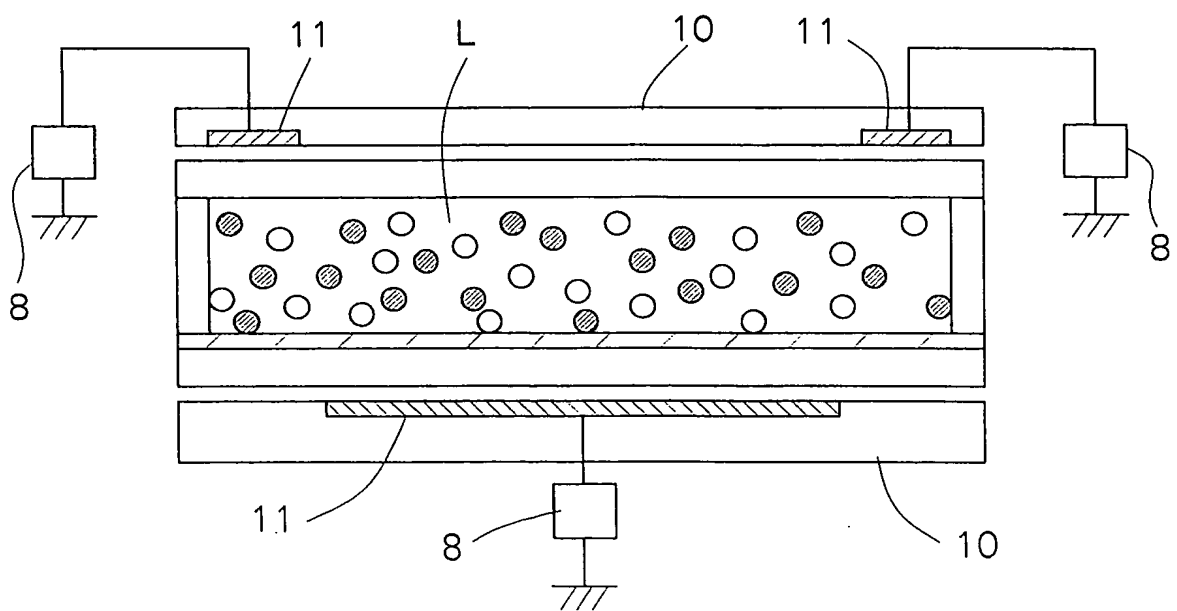


FIG.29

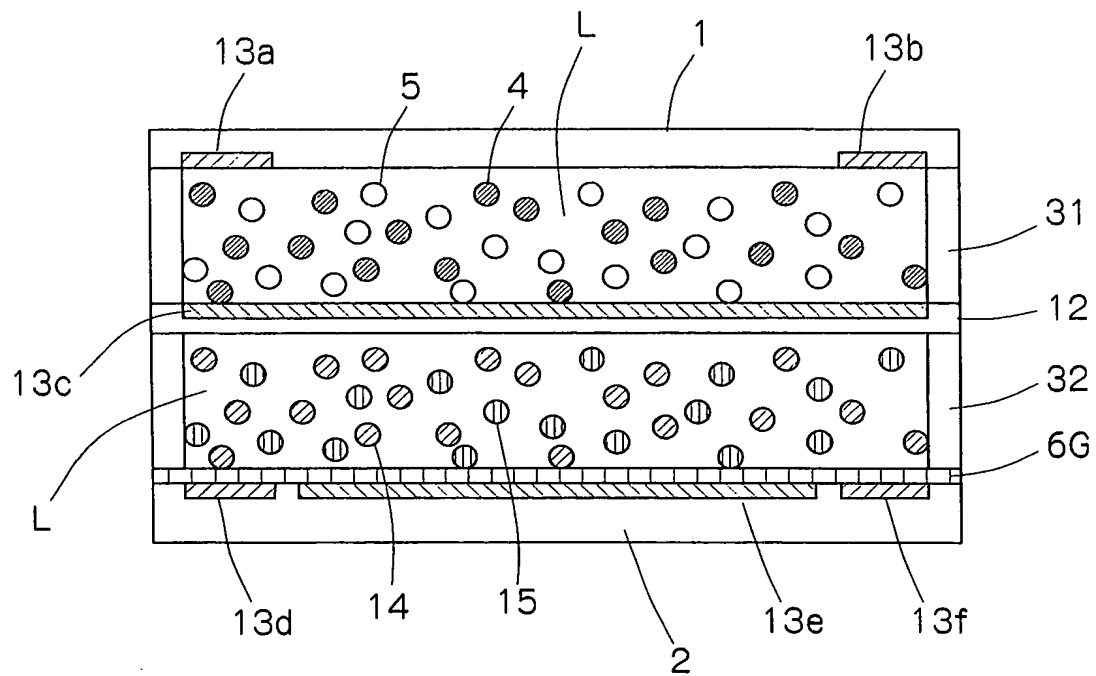


FIG.30

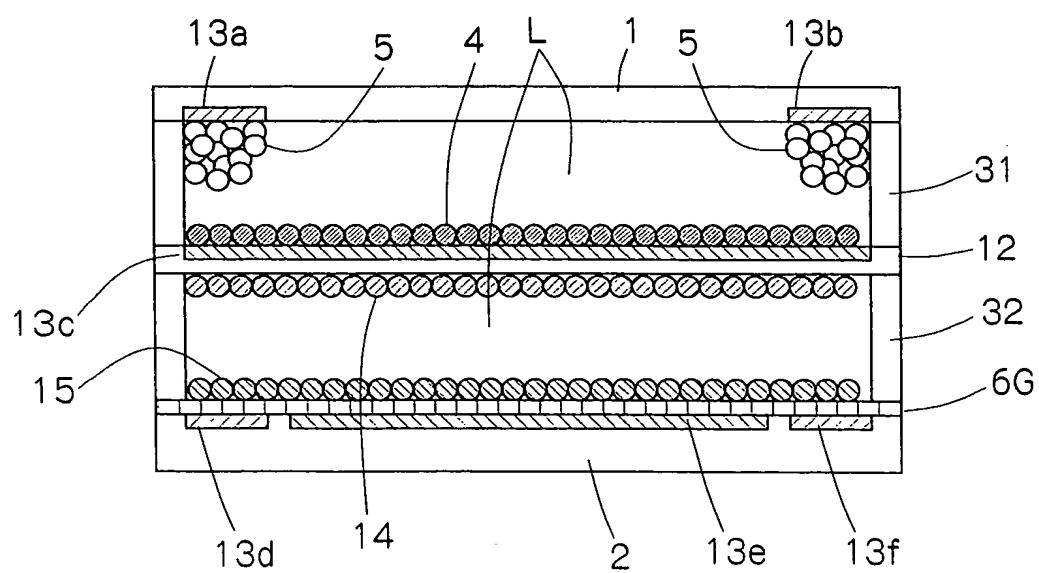


FIG.31

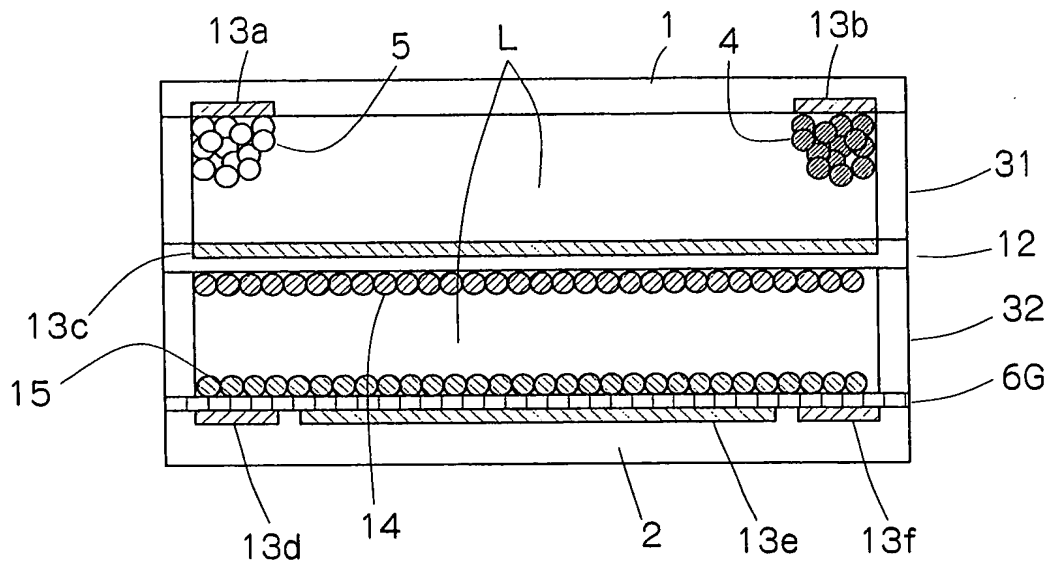


FIG.32

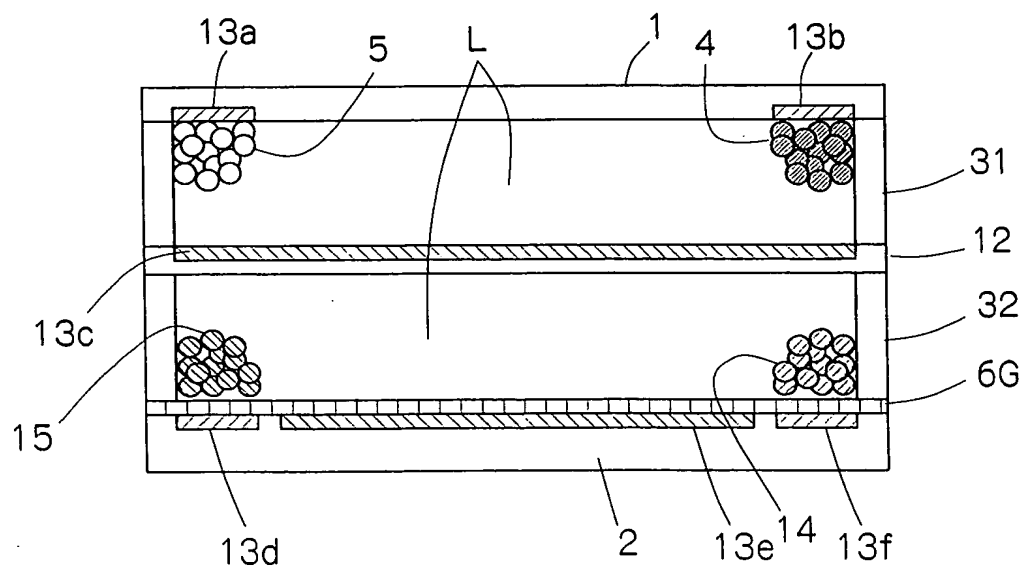




FIG.33

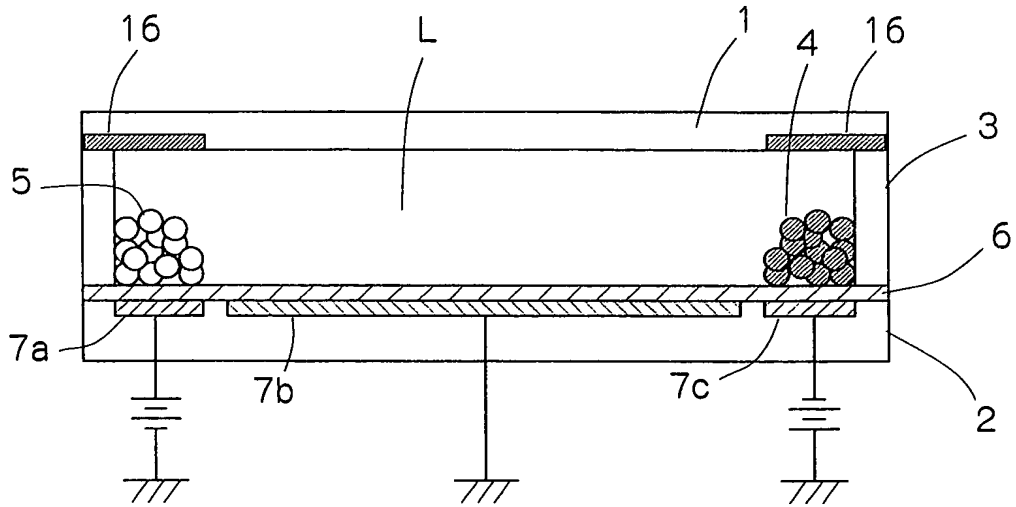


FIG.34

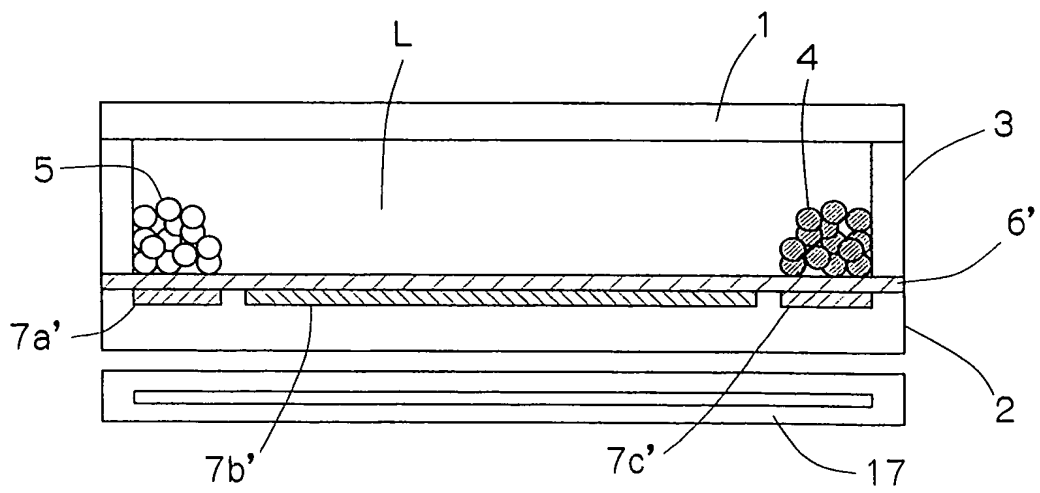


FIG.35

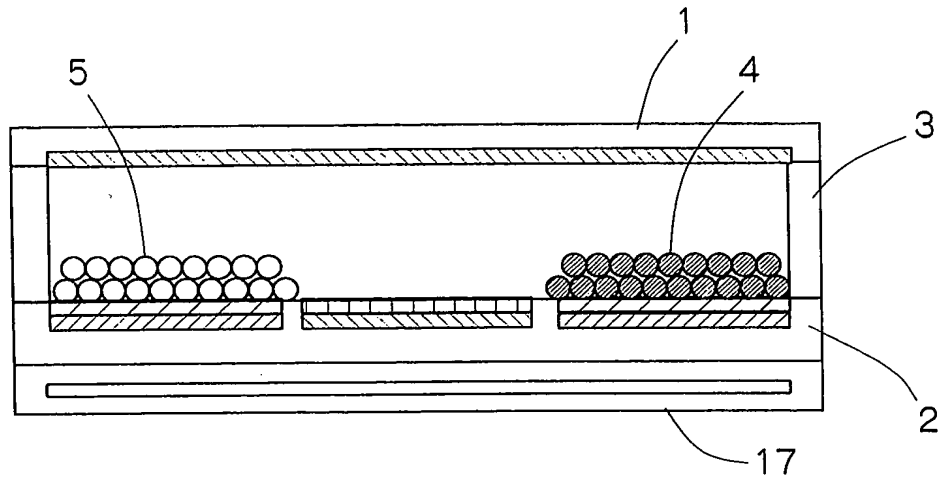


FIG.36

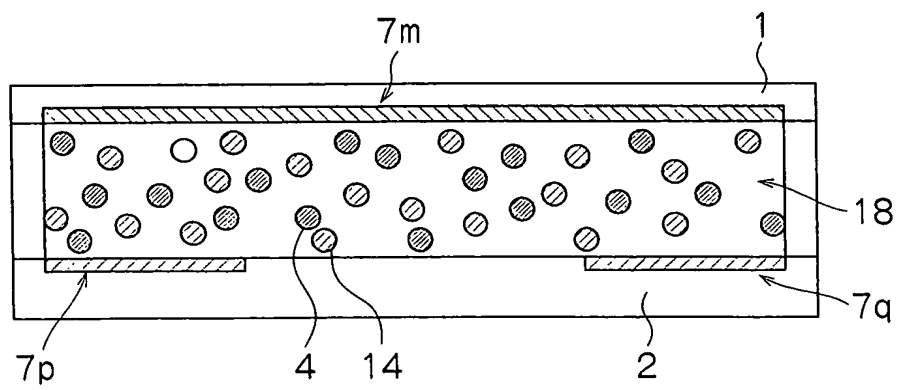


FIG.37

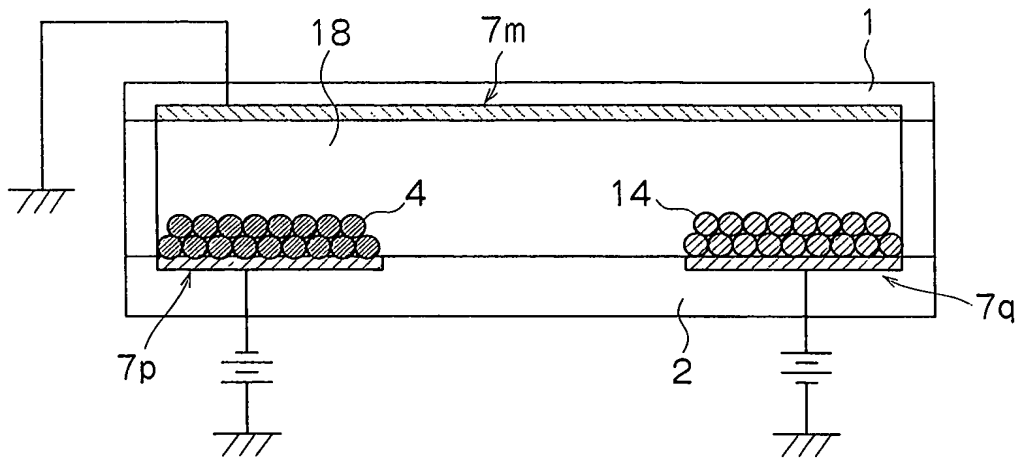


FIG.38

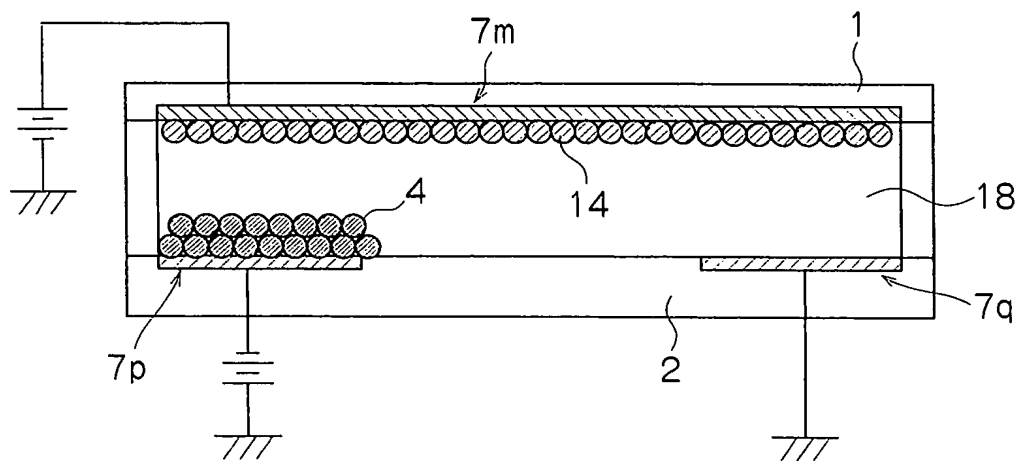


FIG.39

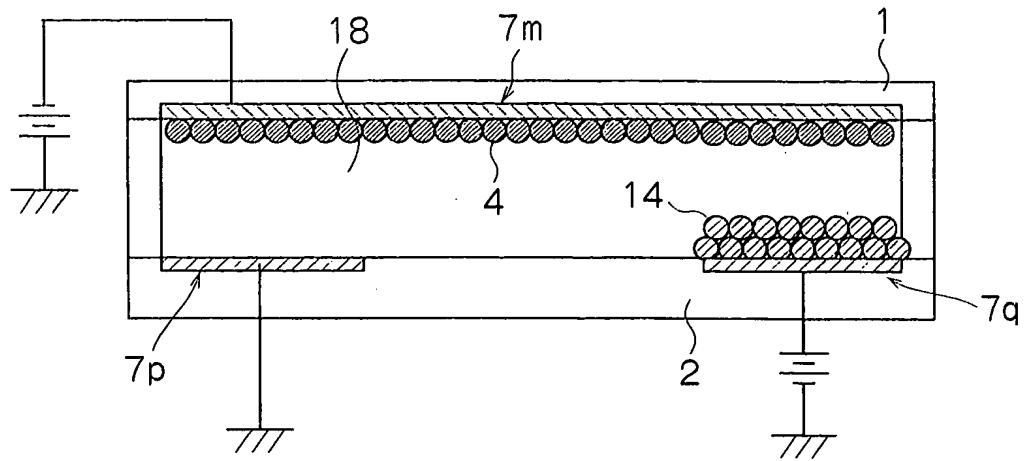


FIG.40A  
PRIOR ART

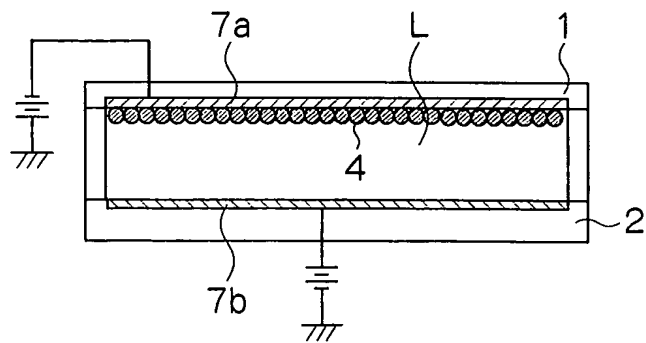


FIG.40B  
PRIOR ART

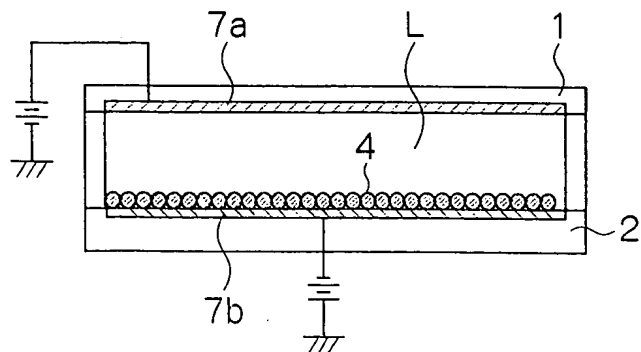


FIG.41A  
PRIOR ART

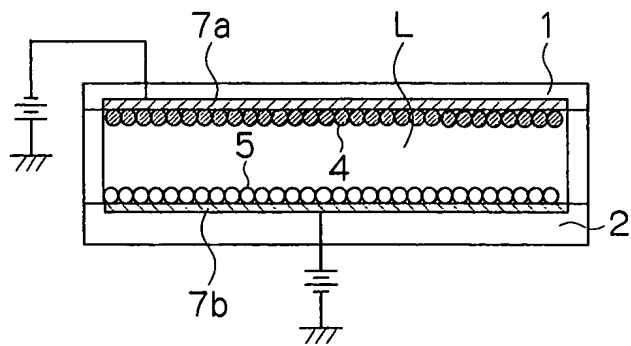


FIG.41B  
PRIOR ART

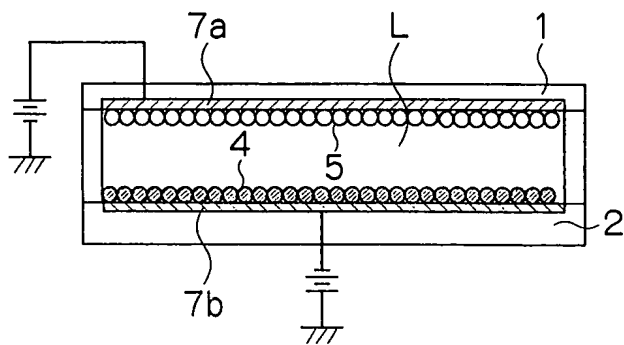


FIG.42A  
PRIOR ART

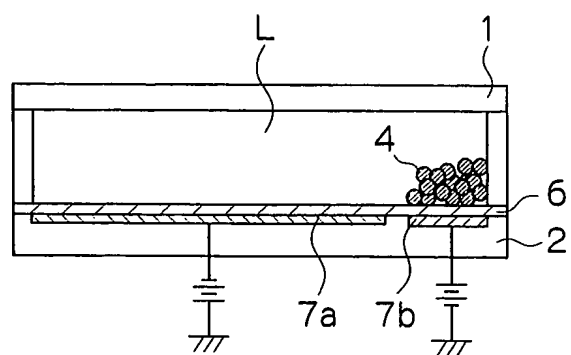


FIG.42B  
PRIOR ART

